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☐	L51	l49 and reorder$3	39
☐	L50	L49 and renam$3	20
☐	L49	l6 and L48	137
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☐	L45	L44 not l43	7
☐	L44	l6 and (reorder buffer)	13
☐	L43	l6 and (reorder buffer) and reservation station$1	6
☐	L42	l6 and (reorder buffer) and scoreboard$3	4
☐	L41	non-predicated instructions	4
		DB=EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=ADJ	
☐	L40	(delay$3 or hold$3 or wait$3 or stall$3) and guard$1 and (dispatch$3 or issu$3)	26
☐	L39	(delay$3 or hold$3 or wait$3 or stall$3) and gaurd$1 and (dispatch$3 or issu$3)	0
☐	L38	(delay$3 or hold$3 or wait$3) and predicate$1 and (dispatch$3 or issu$3)	9
☐	L37	unknown with predicate$1	5
		DB=PGPB,USPT; PLUR=NO; OP=ADJ	
☐	L36	L34 same (dispatch$3 or issu$3)	1
☐	L35	L34 with (dispatch$3 or issu$3)	0
☐	L34	unknown with predicate$1	74
☐	L33	(delay$3 or hold$3 or wait$3) with predicate$1 with (dispatch$3 or issu$3)	19
☐	L32	predicat$3 with stall$3 with (issu$3 or dispatach$3)	1
☐	L31	predicat$3 with block$3 with (issu$3 or dispatach$3)	4
☐	L30	predicate writ$3	13
☐	L29	predicate$1 with stall$3 with instruction$1	20

DB=USOC,EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=ADJ			
☐	L28	predicate$1 with stall$3	9
☐	L27	predicate$1 and (reservation station$1)	3
☐	L26	predicate$1 and (reservation station$1) and scoreboard$1	0
DB=PGPB,USPT; PLUR=NO; OP=ADJ			
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☐	L24	6513109.pn. and reorder and reservation	1
☐	L23	6513109.pn. and reorder	1
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☐	L18	L17 and scoreboard$1	1
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☐	L15	predicate and (reservation station)	10
☐	L14	predicate slip	1
☐	L13	predicate$1 same (reservation station$1) and scoreboard$1	1
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☐	L5	L4 not l3	2
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1 **Continuous delay regulator for controlling recording errors**
Cooper, D.;
Audio and Electroacoustics, IEEE Transactions on , Volume: 14 , Issue: 1 , Mar 1966
Pages:16 - 26

[Abstract] [PDF Full-Text (1192 KB)] **IEEE JNL**

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2 **Correlator compensation requirements for passive time-delay estimation with moving source or receivers**
Adams, W.; Kuhn, J.; Whyland, W.;
Acoustics, Speech, and Signal Processing [see also IEEE Transactions on Signal Processing], IEEE Transactions on , Volume: 28 , Issue: 2 , Apr 1980
Pages:158 - 168

[Abstract] [PDF Full-Text (1008 KB)] **IEEE JNL**

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3 **Simplified design of high-order recursive group-delay filters**
Bernhardt, P.;
Acoustics, Speech, and Signal Processing [see also IEEE Transactions on Signal Processing], IEEE Transactions on , Volume: 28 , Issue: 5 , Oct 1980
Pages:498 - 503

[Abstract] [PDF Full-Text (456 KB)] **IEEE JNL**

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4 **An overview on the time delay estimate in active and passive systems for target localization**

Quazi, A.;
Acoustics, Speech, and Signal Processing [see also IEEE Transactions on Signal Processing], IEEE Transactions on , Volume: 29 , Issue: 3 , Jun 1981
Pages:527 - 533

[Abstract] [PDF Full-Text (752 KB)] **IEEE JNL**

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5 **A Bayesian approach to time delay estimation**
Kenefic, R.;
Acoustics, Speech, and Signal Processing [see also IEEE Transactions on Signal Processing], IEEE Transactions on , Volume: 29 , Issue: 3 , Jun 1981
Pages:611 - 614

[Abstract] [PDF Full-Text (392 KB)] **IEEE JNL**

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6 **Adaptive estimation of time delays in sampled data systems**
Etter, D.; Stearns, S.;
Acoustics, Speech, and Signal Processing [see also IEEE Transactions on Signal Processing], IEEE Transactions on , Volume: 29 , Issue: 3 , Jun 1981
Pages:582 - 587

[Abstract] [PDF Full-Text (560 KB)] **IEEE JNL**

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7 **Time delay estimation for passive sonar signal processing**
Carter, G.;
Acoustics, Speech, and Signal Processing [see also IEEE Transactions on Signal Processing], IEEE Transactions on , Volume: 29 , Issue: 3 , Jun 1981
Pages:463 - 470

[Abstract] [PDF Full-Text (1008 KB)] **IEEE JNL**

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8 **Back cover**

Acoustics, Speech, and Signal Processing [see also IEEE Transactions on Signal Processing], IEEE Transactions on , Volume: 29 , Issue: 6 , Dec 1981
Pages:0 - 0

[Abstract] [PDF Full-Text (2664 KB)] **IEEE JNL**

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9 **Design of recursive group-delay filters by autoregressive modeling**
Yegnanarayana, B.;
Acoustics, Speech, and Signal Processing [see also IEEE Transactions on Signal Processing], IEEE Transactions on , Volume: 30 , Issue: 4 , Aug 1982
Pages:632 - 637

[Abstract] [PDF Full-Text (552 KB)] **IEEE JNL**

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10 **Fundamental limitations in passive time delay estimation--Part I: Narrow-band systems**
Weiss, A.; Weinstein, E.;
Acoustics, Speech, and Signal Processing [see also IEEE Transactions on Signal Processing], IEEE Transactions on , Volume: 31 , Issue: 2 , Apr 1983
Pages:472 - 486

[Abstract] [PDF Full-Text (1336 KB)] **IEEE JNL**

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11 **Delay and Doppler estimation by time-space partition of the array data**
Weinstein, E.; Kletter, D.;
Acoustics, Speech, and Signal Processing [see also IEEE Transactions on Signal Processing], IEEE Transactions on , Volume: 31 , Issue: 6 , Dec 1983
Pages:1523 - 1535

[Abstract] [PDF Full-Text (1112 KB)] **IEEE JNL**

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12 **Subsampling to estimate delay with application to echo cancelling**
Duttweiler, D.;
Acoustics, Speech, and Signal Processing [see also IEEE Transactions on Signal Processing], IEEE Transactions on , Volume: 31 , Issue: 5 , Oct 1983
Pages:1090 - 1099

[Abstract] [PDF Full-Text (832 KB)] **IEEE JNL**

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13 **Delay estimation by expected value**
Bradley, J.; Kirlin, R.;
Acoustics, Speech, and Signal Processing [see also IEEE Transactions on Signal Processing], IEEE Transactions on , Volume: 32 , Issue: 1 , Feb 1984
Pages:19 - 27

[Abstract] [PDF Full-Text (824 KB)] **IEEE JNL**

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14 **The structure and performance of estimators for real-time estimation of randomly varying time delay**
Meyr, H.; Spies, G.;
Acoustics, Speech, and Signal Processing [see also IEEE Transactions on Signal Processing], IEEE Transactions on , Volume: 32 , Issue: 1 , Feb 1984
Pages:81 - 94

[Abstract] [PDF Full-Text (1400 KB)] **IEEE JNL**

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15 **Digital filters with equiripple magnitude and group delay**
Saramaki, T.; Neuvo, Y.;
Acoustics, Speech, and Signal Processing [see also IEEE Transactions on Signal Processing], IEEE Transactions on , Volume: 32 , Issue: 6 , Dec 1984
Pages:1194 - 1200

[Abstract] [PDF Full-Text (744 KB)] **IEEE JNL**

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1 Bounded ignorance: a technique for increasing concurrency in a replicated system
Narayanan Krishnakumar, Arthur J. Bernstein
December 1994 **ACM Transactions on Database Systems (TODS)**, Volume 19 Issue 4

Full text available: pdf(2.84 MB) Additional Information: full citation, abstract, references, citings, index terms, review

Databases are replicated to improve performance and availability. The notion of correctness that has commonly been adopted for concurrent access by transactions to shared, possibly replicated, data is serializability. However, serializability may be impractical in high-performance applications since it imposes too stringent a restriction on concurrency. When serializability is relaxed, the integrity constraints describing the data may be violated. By allowing bounded violations of the integ ...

Keywords: concurrency control, integrity constraints, reachability analysis, replication, serializability

2 Formal aspects of concurrency control in long-duration transaction systems using the NT/PV model
Henry F. Korth, Greg Speegle
September 1994 **ACM Transactions on Database Systems (TODS)**, Volume 19 Issue 3

Full text available: pdf(3.23 MB) Additional Information: full citation, abstract, references, citings, index terms, review

In the typical database system, an execution is correct if it is equivalent to some serial execution. This criterion, called serializability, is unacceptable for new database applications which require long-duration transactions. We present a new transaction model which allows correctness criteria more suitable for these applications. This model combines three enhancements to the standard model: nested transactions, explicit predicates, and multiple versions. These features yield the name o ...

Keywords: concurrency control protocol, semantic information, transaction processing

3 Performance comparison of ILP machines with cycle time evaluation
Tetsuya Hara, Hideki Ando, Chikako Nakanishi, Masao Nakaya
May 1996 **ACM SIGARCH Computer Architecture News , Proceedings of the 23rd**

annual international symposium on Computer architecture, Volume 24 Issue 2

Full text available: pdf(1.48 MB) Additional Information: full citation, abstract, references, citings, index terms

Many studies have investigated performance improvement through exploiting instruction-level parallelism (ILP) with a particular architecture. Unfortunately, these studies indicate performance improvement using the number of cycles that are required to execute a program, but do not quantitatively estimate the penalty imposed on the cycle time from the architecture. Since the performance of a microprocessor must be measured by its execution time, a cycle time evaluation is required as well as a cy ...

4 A method for the development of legal knowledge systems
Pepijn R. S. Visser, Robert W. van Kralingen, Trevor J. M. Bench-Capon
June 1997 **Proceedings of the sixth international conference on Artificial intelligence and law**

Full text available: pdf(1.20 MB) Additional Information: full citation, references, index terms

5 Reasoning about probabilistic algorithms
Josyula R. Rao
August 1990 **Proceedings of the ninth annual ACM symposium on Principles of distributed computing**

Full text available: pdf(1.63 MB) Additional Information: full citation, references, citings, index terms

6 Guarded commands, non-determinacy and a calculus for the derivation of programs
Edsger W. Dijkstra
April 1975 **ACM SIGPLAN Notices , Proceedings of the international conference on Reliable software**, Volume 10 Issue 6

Full text available: pdf(460.92 KB) Additional Information: full citation, abstract, references, citings, index terms

So-called "guarded commands" are introduced as a building block for alternative and repetitive constructs that allow non-deterministic program components for which at least the activity evoked, but possibly even the final state, is not necessarily uniquely determined by the initial state. For the formal derivation of programs expressed in terms of these constructs, a calculus will be shown.

Keywords: Case-construction, Correctness proof, Derivation of programs, Non-determinacy, Program semantics, Programming language semantics, Programming languages, Programming methodology, Repetition, Sequencing primitives, Termination

7 Guarded commands, nondeterminacy and formal derivation of programs
Edsger W. Dijkstra
August 1975 **Communications of the ACM**, Volume 18 Issue 8

Full text available: pdf(480.91 KB) Additional Information: full citation, abstract, references, citings, index terms

So-called "guarded commands" are introduced as a building block for alternative and repetitive constructs that allow nondeterministic program components for which at least the activity evoked, but possibly even the final state, is not necessarily uniquely determined by the initial state. For the formal derivation of programs expressed in terms of these constructs, a calculus will be be shown.

Keywords: case-construction, correctness proof, derivation of programs, nondeterminancy, program semantics, programming language semantics, programming languages, programming methodology, repetition, sequencing primitives, termination

8 A semantics approach for KQML—a general purpose communication language for software agents
Yannis Labrou, Tim Finin
November 1994 **Proceedings of the third international conference on Information and knowledge management**
Full text available: pdf(1.10 MB) Additional Information: full citation, abstract, references, citings, index terms

We investigate the semantics for Knowledge Query Manipulation Language (KQML) and we propose a semantic framework for the language. KQML is a language and a protocol to support communication between software agents. Based on ideas from speech act theory, we propose a semantic description for KQML that associates descriptions of the cognitive states of agents with the use of the language's primitives (performatives). We use this approach to describe the semantics for the basic set of KQML pe ...

9 Applying formal methods to semantic-based decomposition of transactions
Paul Ammann, Sushil Jajodia, Indrakshi Ray
June 1997 **ACM Transactions on Database Systems (TODS)**, Volume 22 Issue 2
Full text available: pdf(569.45 KB) Additional Information: full citation, abstract, references, citings, index terms, review

In some database applications the traditional approach of seerializability, in which transactions appear to execute atomically and in isolation on a consistent database state, fails to satisfy performance requirements. Although many researchers have investigated the process of decomposing transactions into steps to increase concurrency, such research typically focuses on providing algorithms necessary to implement a decomposition supplied by the database application developer and pays relat ...

Keywords: concurrency control, database management systems, transaction processing

10 Correctness conditions for highly available replicated databases
Nancy Lynch, Barbara Blaustein, Michael Siegel
November 1986 **Proceedings of the fifth annual ACM symposium on Principles of distributed computing**
Full text available: pdf(1.68 MB) Additional Information: full citation, references, citings, index terms

11 Revised5 report on the algorithmic language scheme
N. I. Adams, D. H. Bartley, G. Brooks, R. K. Dybvig, D. P. Friedman, R. Halstead, C. Hanson, C. T. Haynes, E. Kohlbecker, D. Oxley, K. M. Pitman, G. J. Rozas, G. L. Steele, G. J. Sussman, M. Wand, H. Abelson
September 1998 **ACM SIGPLAN Notices**, Volume 33 Issue 9
Full text available: pdf(4.44 MB) Additional Information: full citation, citings, index terms

12 Reasoning about probabilistic parallel programs
Josyula R. Rao
May 1994 **ACM Transactions on Programming Languages and Systems (TOPLAS)**,

Volume 16 Issue 3

Full text available: pdf(2.31 MB) Additional Information: full citation, abstract, references, citings, index terms

The use of randomization in the design and analysis of algorithms promises simple and efficient algorithms to difficult problems, some of which may not have a deterministic solution. This gain in simplicity, efficiency, and solvability results in a trade-off of the traditional notion of absolute correctness of algorithms for a more quantitative notion: correctness with a probability between 0 and 1. The addition of the notion of parallelism to the already unintuitive idea of randomization m ...

Keywords: correctness proofs, parallel programming, probabilistic algorithms, programming methodology, specification techniques, verification

13 Selective value prediction
Brad Calder, Glenn Reinman, Dean M. Tullsen
May 1999 **ACM SIGARCH Computer Architecture News , Proceedings of the 26th annual international symposium on Computer architecture**, Volume 27 Issue 2

Full text available: pdf(132.68 KB) Publisher Site Additional Information: full citation, abstract, references, citings, index terms

Value Prediction is a relatively new technique to increase instruction-level parallelism by breaking true data dependence chains. A value prediction architecture *produces* values, which may be later *consumed* by instructions that execute speculatively using the predicted value.This paper examines selective techniques for using value prediction in the presence of predictor capacity constraints and reasonable misprediction penalties. We examine prediction and confidence mechanisms in l ...

14 Mapping a functional specification to an object-oriented specification in software re-engineering
K. Periyasamy, C. Mathew
February 1996 **Proceedings of the 1996 ACM 24th annual conference on Computer science**

Full text available: pdf(878.23 KB) Additional Information: full citation, references, citings, index terms

15 A bi-level language for software process modeling
Gail E. Kaiser, Steven S. Popovich, Israel Z. Ben-Shaul
May 1993 **Proceedings of the 15th international conference on Software Engineering**

Full text available: pdf(1.33 MB) Additional Information: full citation, references, citings

16 Optimizing disjunctive queries with expensive predicates
A. Kemper, G. Moerkotte, K. Peithner, M. Steinbrunn
May 1994 **ACM SIGMOD Record , Proceedings of the 1994 ACM SIGMOD international conference on Management of data**, Volume 23 Issue 2

Full text available: pdf(1.25 MB) Additional Information: full citation, abstract, references, citings, index terms

In this work, we propose and assess a technique called bypass processing for optimizing the evaluation of disjunctive queries with expensive predicates. The technique is particularly useful for optimizing selection predicates that contain terms whose evaluation costs vary tremendously; e.g., the evaluation of a nested subquery or the invocation of a user-defined function in an object-oriented or extended relational model may be orders of magnitude

more expensive than an att ...

17 Case role filling as a side effect of visual search
Heinz Marburger, Wolfgang Wahlster
September 1983 **Proceedings of the first conference on European chapter of the Association for Computational Linguistics**
Full text available: pdf(683.42 KB) Publisher Site
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This paper addresses the problem of generating communicatively adequate extended responses in the absence of specific knowledge concerning the intensions of the questioner. We formulate and justify a heuristic for the selection of optional deep case slots not contained in the question as candidates for the additional information contained in an extended response. It is shown that, in a visually present domain of discourse, case role filling for the construction of an extended response can be reg ...

18 Enhanced modulo scheduling for loops with conditional branches
Nancy J. Warter, Grant E. Haab, Krishna Subramanian, John W. Bockhaus
December 1992 **ACM SIGMICRO Newsletter , Proceedings of the 25th annual international symposium on Microarchitecture**, Volume 23 Issue 1-2
Full text available: pdf(1.21 MB) Additional Information: full citation, references, citings, index terms

19 Objects with logic
Mamdouh H. Ibrahim, Fred A. Cummins
January 1990 **Proceedings of the 1990 ACM annual conference on Cooperation**
Full text available: pdf(608.54 KB) Additional Information: full citation, abstract, references, index terms

This paper describes an approach to the integration of logic and object programming where predicates, logic expressions, and a generalized search protocol that support Prolog-like reasoning are implemented as an integral part of an object-oriented language. This logic programming facility provides 1) domain-based reasoning, 2) functional arguments, 3) support of the abstraction power of object-oriented languages, and 4) matching of complex object patterns, none of which are available in Pro ...

20 Integrating obstacles in goal-driven requirements engineering
Axel van Lamsweerde, Emmanuel Letier
April 1998 **Proceedings of the 20th international conference on Software engineering**
Full text available: pdf(1.30 MB) Publisher Site
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